

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

## In re U.S. Patent Application

IWAHASHI *et al.*

**Application: To be assigned**

**Art Unit: 2824**

**Filed: Concurrently Herewith**

**For: SEMICONDUCTOR INTEGRATED  
CIRCUIT DEVICE**

**Attorney Docket No. HITA.0471**

**Honorable Assistant Commissioner  
for Patents  
Washington, D.C. 20231**

## **INFORMATION DISCLOSURE STATEMENT**

The above-referenced application is a Divisional of U.S. Serial No. 10/339,339 filed Janaury 10, 2003. It includes the same disclosure as U.S. patent application Serial No. 10/339,339.

It is understood that the listed references will be considered in the examination of the application and that no separate copies of the same prior art are required to be provided since they were previously cited or transmitted in the foregoing prior application, pursuant to 37 CFR § 1.98(d). Form(s) PTO 1449 is enclosed listing references cited by the Examining Attorney and submitted by applicant in the prior applications.

This Information Disclosure Statement is submitted with the above-captioned U.S. Divisional application. Accordingly, no fee is due or payable at this time.

The Examiner is requested to acknowledge consideration of the information provided in this paper in accordance with prescribed procedures.

Please charge any additional fees or credit any overpayments in connection with this paper to Deposit Account No. 08-1480.

Respectfully submitted,

\_\_\_\_\_  
Stanley P. Fisher  
Registration Number 24,344

  
Juan Carlos A. Marquez  
Registration Number 34,072

**REED SMITH LLP**  
3110 Fairview Park Drive  
Suite 1400  
Falls Church, Virginia 22042  
(703) 641-4200

**December 15, 2003**

Form PTO 1449  U.S. Department of commerce Patent and Trademark Office  Information Disclosure Statement by Applicant	ATTY. DOCKET NUMBER	SERIAL NUMBER
	HITA.0471	To be assigned
	APPLICANT	
	IWAHASHI et al.	
	FILING DATE	GROUP
	Concurrently herewith	

**U.S. Patent Documents**

Examiner Initial		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB-CLASS	FILING DATE
	*	5,592,414	1/7/97	Soneda et al.			10/25/95
	*	5,559,747	9/24/96	Kasamizugami et al.			6/28/95
	**	5,963,486	10/99	Yeung et al			
	**	5,483,489	01/96	McClure, David C.			
	**	4,509,147	04/85	Tanimura et al.			

**Foreign Patent Documents**

Examiner Initial		DOCUMENT NUMBER	FILING DATE	COUNTRY	CLASS	SUB-CLASS	TRANSLATION	
							YES	No
	*	8-129891	10/28/94	Japan				X
	*	8-63975	8/25/94	Japan				X
	*	9-284100	4/19/96	Japan			Abstract	X
	*	4-60991	6/25/90	Japan				X
	*	4-205787	11/29/90	Japan			Abstract	X
	*	9-245480	3/8/96	Japan			Abstract	X
	*	9-198870	1/24/96	Japan			Abstract	X

**Other Documents (Including Author, Title, Date Pertinent Pages, Etc.)**

EXAMINER	DATE CONSIDERED	
<i>Examiner: Initial if citation is considered, whether or not citation is in conformance with MPEP 609; draw a line through citation if not in conformance and not considered. Include a copy of this form with next communication to applicant</i>		

PTO 1449

\* Cited by Applicant in Parent

\*\* Cited by Examiner in Parent